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## **A Simple Method for Determination of Switched Capacitor Bank in Power Distribution Systems**

**Milad Dowlatshahi and Davood Pahlavan\***

*Islamic Azad University, Khomeinishahr Branch  
Khomeinishahr Azad University  
Isfahan, Iran*

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*This paper proposes an efficient and novel technique for assessment of the direction of switched capacitor bank as well as estimating its distance from the monitoring location in real distribution systems. At first, the proposed method determines the direction of switched capacitor banks based on the slope of the voltage and current space vectors immediately after the capacitor switching instant. Afterward, the exact location of switched capacitor from the monitoring location is determined by estimating the feeder line inductance using first sample magnitude of voltage and current space vector after the switching instant. The discrete wavelet transform (DWT) is also used to detect the exact instant of switching as well as the exact values of voltage and current caused by switching. In this paper, the capabilities of the proposed method are evaluated in various state of switching (normal, Back-To-Back), presence of stochastic nonlinear load and different sampling rate. The effectiveness of the proposed technique is demonstrated with system data obtained from IEEE-13 bus feeders modelled in PSCAD/EMTDC. Simulation results demonstrate the validity and effectiveness of the proposed method considering acceptable error in locating the switched capacitor bank in distribution systems.*

*Keywords: capacitor switching, distance estimation, power quality, stochastic nonlinear load, slope based method, wavelet transform.*

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\* Corresponding author E-mail address: dolatshahi@iaukhsh.ac.ir

# **Простой метод для определения включения групп конденсаторов в системах распределения энергии**

**М. Довлатсхахи, Д. Пахлаван**

*Исламский университет Азад, Кхомейнисахр филиал  
Кхомейнисахр Азад университет  
Исфахан, Иран*

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*Эта статья предлагает эффективный и новый метод для оценки направления коммутуруемых конденсаторных групп, а также оценки расстояния от места наблюдения в реальных системах распределения. Во-первых, предложенный способ определяет направление коммутуруемых конденсаторов, основанных на диаграммах пространственных векторов напряжения и тока, сразу после момента переключения конденсатора. Далее точное расположение включенного конденсатора от места мониторинга определяется путем оценки индуктивности фидерной линии с использованием первого образца пространственного вектора величины напряжения и тока после момента переключения. Дискретное вейвлет-преобразование (DWT) также используется для выявления точного момента переключения и для точных значений напряжения и тока, вызванных переключением. В этой статье возможности предложенного метода оценены в различных вариантах расположения при переключении (нормально, спина к спине), наличия стохастической нелинейной нагрузки и различной частотой дискретизации. Эффективность предложенной методики сопоставляется с данными, полученными из системы IEEE-13bus фидеров, смоделированных в PSCAD / EMTDC. Результаты моделирования показывают обоснованность и эффективность предложенного метода с учетом приемлемой ошибки в поиске включения конденсаторов в системах распределения.*

*Ключевые слова: переключение конденсаторов, оценка расстояния, качество питания, стохастическая нелинейная нагрузка, базовый метод, вейвлет-преобразование.*

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## **1. Introduction**

The capacitor banks in power system are used to improve the voltage profile, provide the reactive power, improve the power factor, reduce the power losses and increase the system capacity. Regarding the wide use and the repetitive switching of the switching capacitor banks, the transient states resulted from their switching have negative effects on power quality and equipment involved in distribution systems [1]. The examples of some destructive effects in distribution systems are transient disturbances which lead to the tripping of adjustable speed drives, failure of control and protective equipment, power electrical tools and all sensitive equipment in the power system [2]. Therefore, to predict and enhance the power quality, at the first place, it is necessary to identify the transient state of capacitor bank switching by use of a limited number of power quality meter (PQM) tools and then determining location of the turbulence origin. The focus of this paper is on development of a fast method for exact locating a switched capacitor in a power system. Until present, in order to determine the location of capacitor bank, the various techniques have been under taken. In [3], an introduced method to determine the switching capacitor bank location by use of line impedance and resonant frequency. However, this method requires solving the dynamic equations of the system and it is not proper for real power system. [4], a proposed method to track the transient state of the capacitor by use of indicators for

energy and power changes resulted from the current and voltage transient. However, the considering issues have some approximation which may cause to unacceptable error in distance estimation of SCB. The backward-Kalman filter was applied in [5] to estimate the location of switched capacitor bank. Application of this method requires solving the dynamic equations in the power system and it seems to be impractical for real distribution systems. The capacitor bank determined approximately in [6] by use of the initial voltage changes. However, proposed method for those capacitor banks which were located at the downstream of the monitoring location and it would be unreliable in real distribution systems. In [7], using the signs of voltage and current waveform gradients as well as power factor changes, a method for upstream and downstream of the monitoring location is proposed. This method relies on the measurement of the system power factor while it is practically impossible to measure precisely in real power systems. Moreover this method the location of capacitor switching is not accurate enough. Considering voltage signed by hybrid principle component analysis proposed in [8]. In this method, a classification system composed of two neural networks which used to reduce the input data, while in the practical point of view this method in a large system is complicated. A method based on the artificial neural network (ANN) introduced in [9]. In which method, the transient duration and the current energy of transient state resulted from two levels of the discrete wavelet transform are used to train the neural network. A method is proposed in [10] to assessment of the direction and estimating the distance between the capacitor bank and monitoring location by the transient current and voltage of each phase. The efficiency and the precision of this method is totally depend on the switching instant so that the switching at each time except for the instant of the crossing the voltage at peak, will lead to error in determining the direction and distance estimation. However the introduced index ( $k$ ) for each phase at every instant of switching would be different. Moreover this, the effect of the laterals on the distance estimation algorithm was ignored. The capacitor bank location determined in [11] by use of the magnitude of the voltage and current space vector. This method just determined where the capacitor bank exist but it couldn't locate exactly the switched capacitor bank in the mentioned branch and just determines approximately the range of switching capacitor bank, while it is possible that there are several capacitors candidate for switching in the specified range and at the intended instant.

In this article, a novel and efficient method to assessment of the direction and location of switched capacitor bank in presence of stochastic nonlinear loads in the real distribution system is introduced. Problem and hypothesis description is indicated in (2), the determining the direction algorithm of switched capacitor bank will be introduce in (3) and the method to estimate the distance from monitoring location will proposed in (4). The efficiency and authenticity of the proposed method in IEEE-13 bus network [12] is designed and studied by PSCAD software [13] and the results of these simulations are shown in section (5).

## 2. Problem and Hypothesis Description

This article focuses on designing a fast algorithm to locate the switched capacitor bank by use of the data obtained from the transient state resulted from capacitor bank's switching. In this method, the switching capacitor bank is considered without any control mechanism for reducing transient overvoltage and the inrush current due to the switching. Switching transients without control mechanisms are more likely to cause power quality problems than those with control mechanisms. Switching without control mechanisms can well represent the most of capacitor banks in distribution feeders. Fig. 1 shows the

Block-Diagram of the proposed algorithm. As it's shown in this fig, detection and classification of the disturbance origin in the transient state [14-16], as well as, the direction in which the capacitor bank is switched and also its distance respect to the monitoring location is determined by extracting the necessary data from (PQM).

The single-line diagram of the sample distribution network with the switching capacitor bank is shown in Fig. 2. In this network, branch (2) represents a direction where a switching capacitor bank is located and branch (3) indicates an equivalent circuit of all branches which are not located in the direction of switching capacitor bank. Therefore, each arbitrary distribution network, like a sample distribution network in Fig. 2, can be considered as two branches that one is a switching capacitor bank feeder and the other is the equivalent circuit of those branches which is not locate in the path where feeding the switching capacitor bank. Basically, the frequency of the power system is much lower than the switching frequency. Therefore, at the instant of switching,  $V_{sc}$  is considered as constant.

When a capacitor considering no initial charge is switched on, it can be modelled as short circuit and therefore there will be a sudden decrease in network voltage level ( $PQM_{1,2}$ ) due to continuity of the capacitor voltage ( $V(t_s^+) < V(t_s^-)$ ). There is also an expected energizing current to charge the capacitor bank immediately after the switching instant along the direction of real power flow. As it's represented by Fig. 3, due to the capacitor bank charge, the current will be increased in those branches which are supplying capacitor bank and vice versa, it will be reduced in other branches. These changes are demonstrated considering equation (1) and (5):

$$\begin{cases} i_2(t_s^-) = i_1(t_s^-) \times \frac{Z_3 + Z_{L1}}{Z_3 + Z_{L1} + Z_2 + Z_4 + Z_{L2}} \\ i_2(t_s^+) = i_1(t_s^+) \times \frac{Z_3 + Z_{L1}}{Z_3 + Z_{L1} + Z_2} \end{cases} \Rightarrow i_2(t_s^+) > i_2(t_s^-) \quad (1)$$

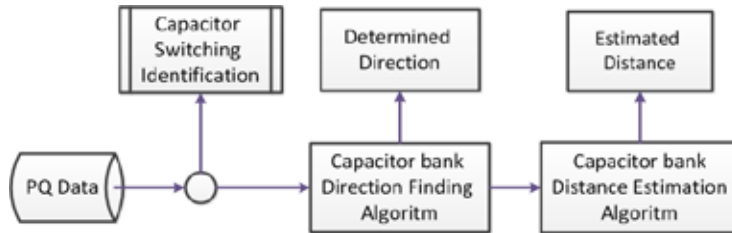


Fig. 1. General algorithm of the proposed method

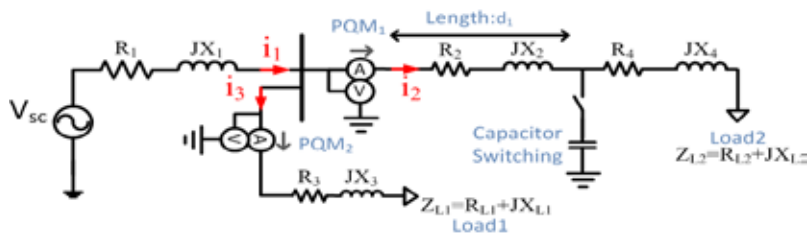


Fig. 2. Sample distribution network

$$L_{eq_3} = L_3 + L_{L_1} \quad , \quad R_{eq_3} = R_3 + R_{L_1} \quad (2)$$

$$\begin{cases} V(t_s^-) = L_{eq_3} \frac{di_3(t_s^-)}{dt} + R_{eq_3} i_3(t_s^-) \\ V(t_s^+) = L_{eq_3} \frac{di_3(t_s^+)}{dt} + R_{eq_3} i_3(t_s^+) \end{cases} \quad , \quad \Delta V(t_s) = V(t_s^+) - V(t_s^-) < 0 \quad (3)$$

$$\Delta V(t_s) = L_{eq_3} \frac{d\Delta i_3}{dt} + R_{eq_3} \Delta i_3 \quad (4)$$

Because at switching instant  $X \gg R$ :

$$\begin{cases} \Delta V(t_s) = L_{eq_3} \frac{d\Delta i_3}{dt} \Rightarrow \Delta i_3 < 0 \\ \Delta V(t_s) < 0 \\ \Delta i_3 = i_3(t_s^+) - i_3(t_s^-) < 0 \Rightarrow i_3(t_s^+) < i_3(t_s^-) \end{cases} \quad (5)$$

It is noteworthy that, the amount of voltage drop in the system at the monitoring location depends on the impedance of the line between switched capacitor bank and monitoring location so that the shorter distance between the switched capacitor bank and the monitoring location (PQM1), would cause the  $(V(t_s^+))$  to be closer to zero which is shown in Fig. 4

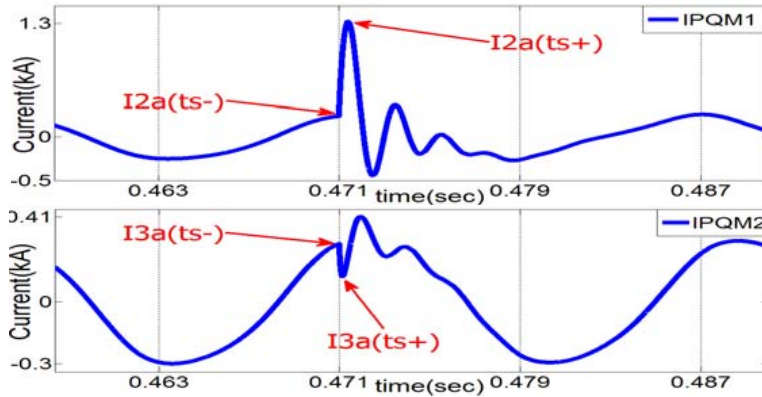


Fig. 3. Waveform of the currents resulting from the capacitor bank switching in the sample network

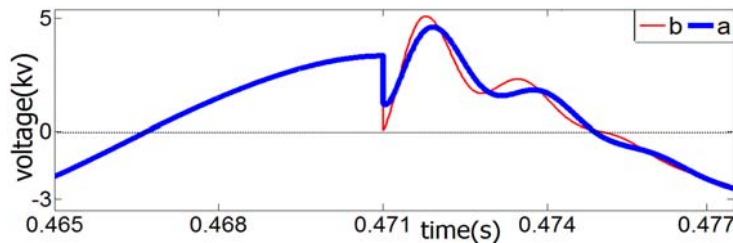


Fig. 4. Waveform of the currents resulting from the capacitor bank switching (a) considering the line between monitoring location and capacitor, (b) without line between monitoring location and capacitor

### 3. Switched Capacitor Bank Direction Determination

As a result of capacitor bank switching, the changes in the current and the voltage at the beginning of the transient state may have negative or positive slopes. The slope-based method, determines the switched capacitor bank location respect to the monitoring location, immediately after the switching by use of the current and voltage signals measured by PQM.

#### 3.1. Direction determination using the slope of the current and the voltage of each phase in the transient state

In this section, a method is introduced which determines the direction where the capacitor bank is switched, using the changes in the current and the voltage of each phase immediately before and after the switching instant. The slope-based method indicates that the slope of the current and the voltage of each phase immediately before and after switching will have the opposite polarities for the monitoring location placed in line and branches which feed the capacitor bank and identical polarities in the other lines and branches the magnitude of changes in current and voltage waveforms depend on the size and location of the switched capacitor bank [10].

$$D_{VI} = (V(t_s^+) - V(t_s^-)) \times (i(t_s^+) - i(t_s^-)) \quad (6)$$

If switching occurs at the instant  $t=t_s$ ,  $t=t_s-$  represents the sampling time before the switching instant and  $t=t_s+$  shows sampling time after switching instant. Therefore, considering the slope of the current and voltage for each phase in the transient state and comparison of its changes, the direction in which the capacitor bank is switched can be found.

Figs. 5, 6 shows the switching waveform of the transient current and voltage of the phase a at the instant of crossing the voltage at peak  $t_s=0.471$  (state (a)) and at the instant of crossing voltage at around zero  $t_s=0.283$  (state (b)) in the distribution system.

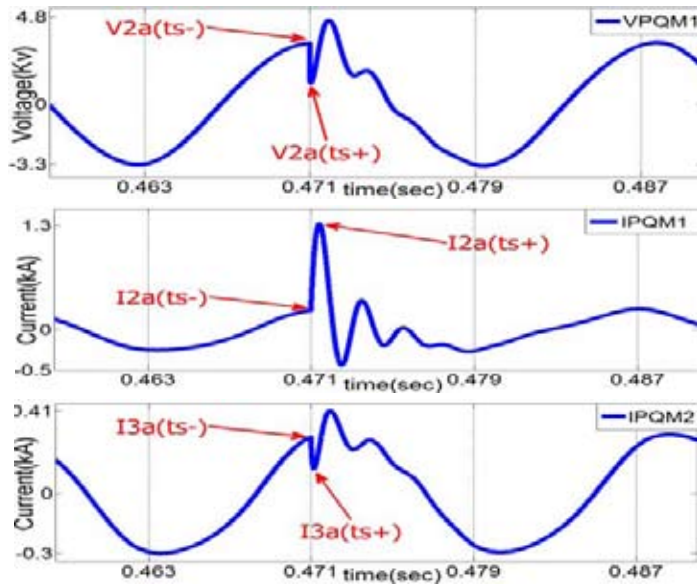


Fig. 5. Waveform of the transient voltage and current around the instant of crossing the voltage at peak (state (a))

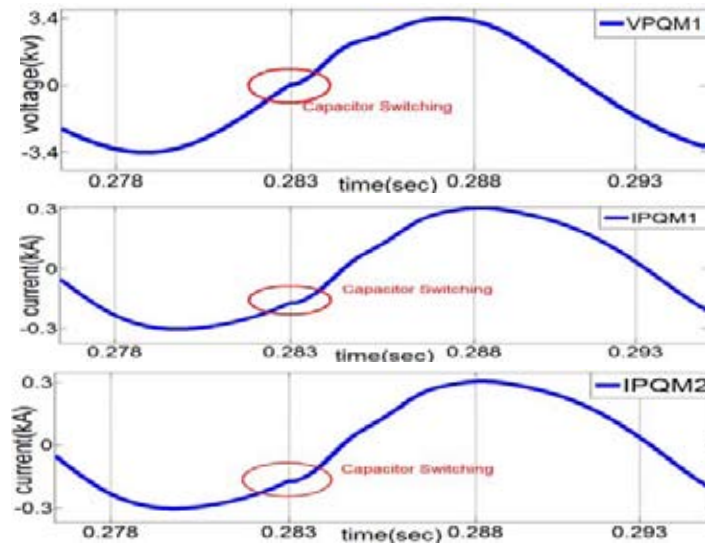


Fig. 6. Waveform of the transient voltage and current around the instant of crossing the voltage at around zero (state (b))

Table 1.  $D_{VI}$  index for the sample network considering two different instant

Branch	state(a)		state(b)	
	$D_{VI}$	Sign	$D_{VI}$	Sign
1	-2.31	Negative	-0.014	Negative
2	<b>-2.38</b>	<b>Negative</b>	-0.01	Negative
3	+0.348	Positive	-0.003	Negative

According to Fig. 2, since the capacitor bank located in the branch (2), the  $D_{VI}$  index in the branches (1) and (2) are negative while in the branch (3) is positive and magnitude of the  $D_{VI}$  index in the branch (2) is larger than other branches. it can be seen in **Table 1** that direction determination at the instant of crossing the voltage at peak (state (a)) is correct, however, when the switching instant is close to the zero-crossing point of the waveform (state (b)), the direction finding is not accomplished correctly. Since the polarity of the index ( $D_{VI}$ ) is negative in all branches (all three branches feed the capacitor bank) and the value of the index ( $D_{VI}$ ) in branch (1) is larger than the other branches (the capacitor is located in branch (1)), while according to Fig. 2, the capacitor is located in branch (2). Therefore, it can be concluded that assessment of the direction of switched capacitor bank in the described method based on the gradients of the voltage and current in transient state in each phase is totally dependent on the switching instant of the capacitor bank.

In the slope-based methods, when the switching instant is close to the zero-crossing point of the waveform, the transient state resulted from switching is too small [11] and determining sign of the  $D_{VI}$  index is not authentic in this situation and should be determined based on the other phases. Also in real situation, in the environments infected with harmonics and noises, current and voltage waveforms are no more sinusoidal hence, determining the slope of the capacitor bank switching at

the instant of crossing the signal at zero is complicated and fallacious because of the small transient state.

3.2. Switched capacitor bank determination using the slope of voltage and current space vectors

A method which proposed in [11] determined the direction of the switched the capacitor bank by the slope of the voltage and current space vector. The main advantage of the space vector method compared to other filter-based methods is that this method is less dependence on switching instant because all three-phase quantities contribute to the magnitude of the space vector and works considering a quantity that represents contributions of the three-phase measured waveforms. The other advantage of this method is its algebraic nature which yield facilitates its function and implementation. Fig. 7 shows a block diagram based on the space vector.

The magnitude of space vector of a value for three phases ( $f_{abc}$ ) is calculated using equation (7). The symbol  $\| \cdot \|$  represents a quadratic norm function applied to the components of voltages and currents in an arbitrary reference frame (e.g. qd0- or  $\alpha\beta 0$ -frame). The advantage of using  $\|v\|$  and  $\|i\|$  instead of phase quantities is that the space vector quantities encounter the information of all three phases at the switching instant. The measured abc voltages and currents are introduced to a space-vector calculator, which provides two non-zero quantities corresponding to the voltages and currents. In this method, finally the magnitude of the space vector is used in the reference  $\alpha\beta 0$  to determine the direction of switched capacitor bank. The most important advantage of using  $\alpha\beta 0$  compared that dq0 frame is that it enables calculating the magnitudes of the voltage and current vectors without using a phase-locked loop (PLL) for synchronisation, In this frame, the abc components are directly converted to the fixed transformation matrix  $K_0$ [17].

$$K_0 = \frac{2}{3} \begin{bmatrix} 1 & -0.5 & -0.5 \\ 0 & \frac{\sqrt{3}}{2} & \frac{\sqrt{3}}{2} \\ 0.5 & 0.5 & 0.5 \end{bmatrix}, [f_{dq0}] = [K_0] \cdot [f_{abc}] \tag{7}$$

$$\|f\| = \sqrt{f_d^2 + f_q^2 + f_0^2}$$

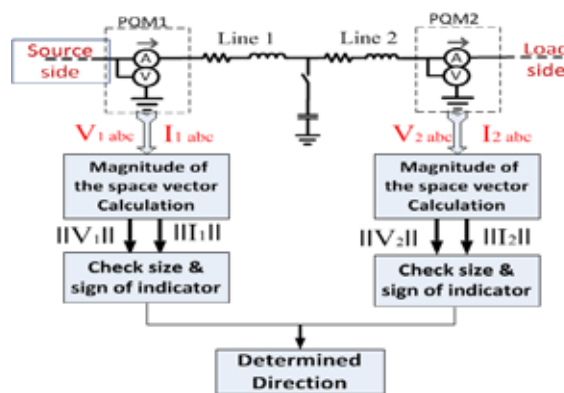


Fig. 7. Block-Diagram of the method based on the space vector of the current and voltage



In this state, by calculating the index based on gradient of the transient voltage and current space vector ( $\|D_{V1}\|$ ), the direction of switching capacitor bank is determined using the following equation:

$$\|D_{V1}\| = (\|V(t_s^+)\| - \|V(t_s^-)\|) \times (\|i(t_s^+)\| - \|i(t_s^-)\|) \quad (8)$$

Where  $(\|V(t_s^-)\|), (\|V(t_s^+)\|)$  are the magnitude of the voltage space vector immediately before and after the switching instants and  $(\|i(t_s^-)\|), (\|i(t_s^+)\|)$  are the magnitude of the current space vector immediately before and after switching instants.

Figs. 8, 9 show the space vectors of the current and voltage for the sample distribution network at two instants when crossing the voltage signal at peak ( $t_s=0.47$ ) and crossing around zero ( $t_s=0.283$ ). According to Table 2, it can be seen that the index based on gradient of the transient voltage and current space vector ( $\|D_{V1}\|$ ), has correctly identified the direction of the switched capacitor bank in both states (a) and (b). Since all three-phase quantities contribute to the magnitude of the space vector and works with a quantity that represents contributions of the three-phase measured waveforms, this method is almost independent from the switching instant.

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### 3.3. Switched Capacitor bank Direction Determination Represented by Flowchart

After detection and classification the disturbance origin in the transient state, if the disturbance caused by the capacitor bank switching, extracting the transient current and voltage values measured

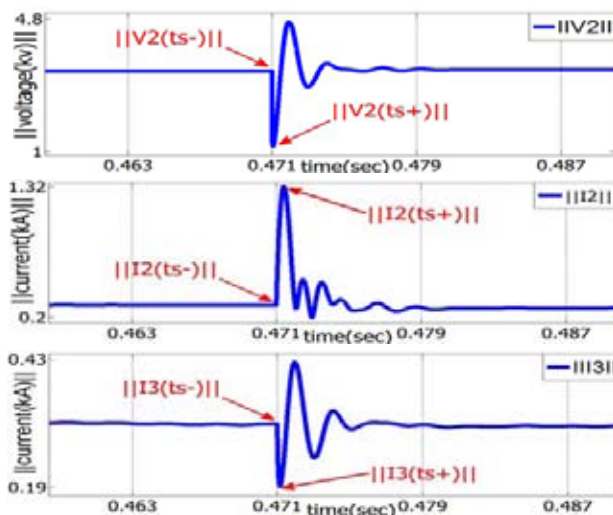


Fig. 8. Space vector of the current and voltage at the instant of crossing the signal at peak in the sample network (state(a))

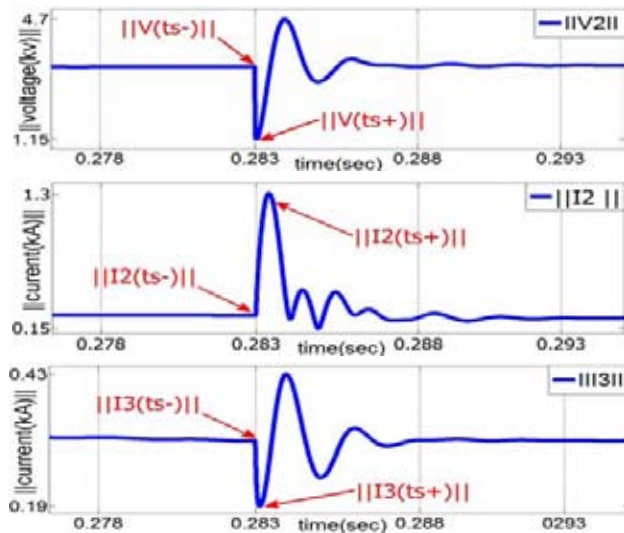


Fig. 9. Current and voltage Space vector magnitude at the instant of crossing the signal by around zero in the sample network (state (b))

Table 2.  $D_{VI}$  index considering the space vector gradient of the current and voltage

Branch	state(a)		state(b)	
	$D_{VI}$	Sign	$D_{VI}$	Sign
1	-2.14	Negative	-2.115	Negative
2	-2.28	Negative	-2.257	Negative
3	+0.25	Positive	+0.2457	Negative

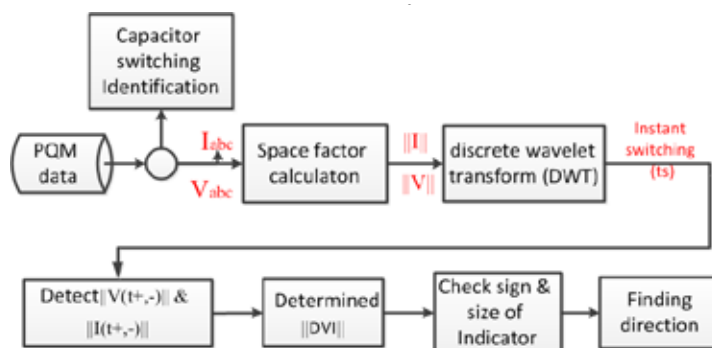


Fig. 10. The flowchart which represents the diagram direction finding for switched capacitor bank

by PQM and considering the method based on the space vector gradient, the indicator  $||D_{VI}||$  can be calculated. The following diagram represents the direction of switched capacitor bank determination in the real distribution system which is shown in Fig. 10.

#### 4. Estimating the Distance between the Switching Capacitor Bank and the Monitoring Location

After determining direction of the switched capacitor bank using the  $\|D_{V1}\|$  index, if there is only one switching capacitor bank, it will not be necessary to estimate the distance, however, if in the desired branch, there are more than one switching capacitor banks estimating of the distance respect to the monitoring location in order to identify the disturbance origin is necessary. For example, the  $\|D_{V1}\|$  index indicates that there is a switched capacitor located in the branch 2, but to determine which capacitor banks (C1 or C2) in the desired branch caused disturbance, the distance estimation can be efficient. Fig. 11 shows the sample distribution network with two switched capacitors where placed in the branch 2.

Two approximately similar methods to estimate distance between the capacitor bank and the monitoring location are proposed in [6] and [10] using circuits theories and the initial changes of the voltage due to capacitor bank switching. The proposed method in [6] is introduced only for the category of capacitor banks that are placed at the downstream of the monitoring location and the distance estimation algorithm totally depends on switching instant so that the capacitor bank switching at each instant except for the time of crossing the voltage signal at peak, will result in many errors in distance estimation. In the represented algorithm in [10] which depending totally on the switching time, the effect of reactance laterals branches on estimating distance between the capacitor bank and the monitoring location ignored. In other words, in this method, generally, load inductance is considered very larger than thevenin and line inductances (more than 10 times), but if the number of system loads in the branch is large, since these loads are parallel, the inductance of equivalent load decreases intensely and the current from the same branch will be increased which can cause the error of distance estimation rises up to 40 %.

In this section, considering these two methods as well as application of changes in the magnitude of space vector of the current and voltage immediately after switching an efficient and novel technique to estimate distance between the capacitor bank and the monitoring location in a real distribution system will be introduced. Fig. 12 shows the equivalent circuit of the sample distribution network shown in Fig. 11.

In this section, toward estimating the capacitor bank distance considering the reactance effect on laterals in the each phase, the relations in the space vector domain is expressed.

It is assumed in this network that capacitor  $C_1$  is switched at the instant  $t_s$ . It is noteworthy that due to switching of the capacitor  $C_1$ , the voltage at bus 3 immediately after switching ( $V(t_s^+)$ ), is not

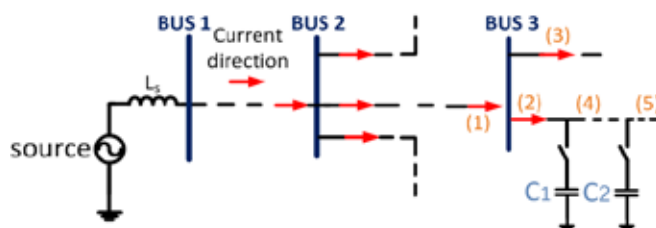


Fig. 11. The sample distribution system with two switching capacitors in the branch (2)

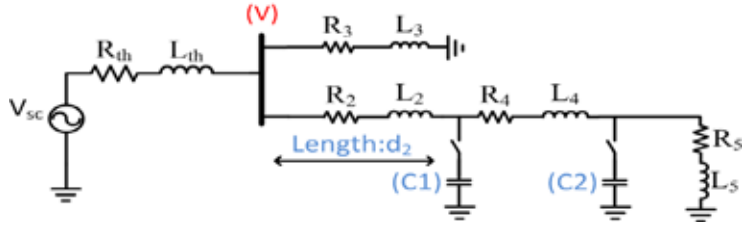


Fig. 12. Equivalent circuit of the distribution network shown in Fig. 11

dependent on parameters of the branches (4) and (5). Because the capacitor voltage at the switching instant is zero, therefore, all lines and downstream loads from the capacitor bank at the instant ( $V(t_s^+)$ ) are ignored.

$V_{sc}(t_s^-)$  represents the system voltage before the switching instant which considered constant since the power system at fundamental frequency is substantially lower than a typical capacitor switching frequency. Voltage drop between  $V_{sc}(t_s^-)$  and  $V(t_s^-)$  can be ignored therefore, in (9),  $V(t_s^-)$  can be replaced by  $V_{sc}(t_s^-)$ , therefore:

$$L_{eq} = \frac{L_2 L_3}{L_2 + L_3} \quad (9)$$

$$V(t_s^+) = \frac{L_{eq}}{L_{eq} + L_{th}} \times V_{sc}(t_s^-) \Rightarrow V(t_s^+) = \frac{L_{eq}}{L_{eq} + L_{th}} \times V(t_s^-) \quad (10)$$

$$L_{eq} = \frac{V(t_s^+)}{V(t_s^-) - V(t_s^+)} \times L_{th} \quad (11)$$

$$\Rightarrow L_{eq} = K \times L_{th} \quad (12)$$

Considering (9), (12)  $L_2$  can be obtained from the following equation:

$$\Rightarrow L_2 = K \frac{L_{th} L_3}{L_3 + L_{th}} \quad (13)$$

On the other hand, since at the instant ( $t_s^+$ ) the branches 2 and 3 are parallel, the following relation can be written:

$$L_2 \frac{di_2(t_s^+)}{dt} = L_3 \frac{di_3(t_s^+)}{dt} \Rightarrow \frac{L_2}{L_3} = \frac{di_3(t_s^+)}{di_2(t_s^+)} = \frac{i_3(t_s^+) - i_3(t_s^-)}{i_2(t_s^+) - i_2(t_s^-)} = \psi \quad (14)$$

$$\begin{cases} L_2 = K \frac{L_{th} L_3}{L_3 - K L_{th}} \Rightarrow L_2 = K L_{th} (1 + \psi) \\ \frac{L_2}{L_3} = \psi \end{cases} \quad (15)$$

$$L_2 = d_2 \times L_u \Rightarrow d_2 = K \times \frac{L_{th}}{L_u} (1 + \psi) \tag{16}$$

where  $i_2(t_s^+)$ ,  $i_3(t_s^+)$  represent respectively the current value of the branch (2) and (3) immediately after the instant ( $t_s$ ),  $i_2(t_s^-)$  and  $i_3(t_s^-)$  represent respectively the current value of the branch (2) and (3) immediately after the instant ( $t_s^-$ ) and ( $\psi$ ) is introduced as the ratio of the changes in the current of the branch 3 to the changes in the current of the branch 2.

In relation (16),  $L_u$  is the inductance per-unit length of line and calculated based on the system specifications.  $V(t_s^+)$  and  $V(t_s^-)$  values are calculated based on the transient voltage waveforms and  $d_2$  represents the distance between the switched capacitor bank and the monitoring location. Thevenin inductance ( $L_{th}$ ) can be calculated by short-circuit current in the monitoring location or considering only the inductance of the main feeder from the monitoring location to the source without considering laterals. If the monitoring location is not far from the source, this simplification does not make a significant error.

If according to Fig. 13, there are the ( $n$ ) branches in the monitoring location (bus 3), the distance between the switched capacitor bank and the monitoring location ( $d_2$ ) is calculated by relation (17).

$$d_2 = K \frac{L_{th}}{L_u} (1 + \Psi_1 + \dots + \Psi_n) \Rightarrow \Psi_n = \frac{i_n(t_s^+) - i_n(t_s^-)}{i_2(t_s^+) - i_2(t_s^-)} \tag{17}$$

In (17), taking into account the reactance of laterals, provides an estimation of the switched capacitor bank distance in each phase. Due to the phase difference, the voltage of each phase at the switching instant is in a different situation and because parameter ( $K$ ) depends on voltage value immediately before and after switching instant, parameter ( $K$ ) in each phase is different from two other phases which can results in estimating three distances from monitoring location so that except for capacitor bank switching right at the moment when voltage crossing at the peak, none of them will be near to reality.

As capacitor bank switching may occur at every moment (including switching at peak, zero crossing or between them) and distance estimation in the method based on the each phase, is totally dependence on the switching instant, the method based on the space vector is presented to estimate the switching capacitor bank distance from the monitoring location and the distance estimation is not only independent from the switching instant, but also is calculated one unit for three phases  $K$  ( $K_{s,v}$ ) which includes the information of three phases.

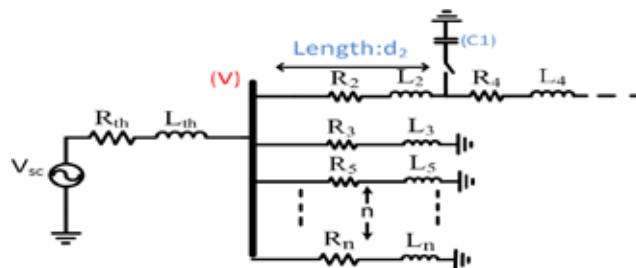


Fig. 13. The sample distribution network with ( $n$ ) branches in the monitoring location

(18) to (32) represent how to estimate capacitor banks distance considering inductance laterals in the space vector domain.

Regarding (10), it can be written:

$$\begin{cases} V_a(t_s^+) = \frac{L_{eq}}{L_{th} + L_{eq}} V_a(t_s^-) \\ V_b(t_s^+) = \frac{L_{eq}}{L_{th} + L_{eq}} V_b(t_s^-) \\ V_c(t_s^+) = \frac{L_{eq}}{L_{th} + L_{eq}} V_c(t_s^-) \end{cases} \Rightarrow \begin{bmatrix} V_a(t_s^+) \\ V_b(t_s^+) \\ V_c(t_s^+) \end{bmatrix} = \frac{L_{eq}}{L_{th} + L_{eq}} \begin{bmatrix} V_a(t_s^-) \\ V_b(t_s^-) \\ V_c(t_s^-) \end{bmatrix} \quad (18)$$

$$KV_{abc}(t_s^+) = \frac{L_{eq}}{L_{eq} + L_{th}} KV_{abc}(t_s^-) \Rightarrow V_{dqo}(t_s^+) = \frac{L_{eq}}{L_{eq} + L_{th}} V_{dqo}(t_s^-) \quad (19)$$

In such state, according to (7), voltage magnitude can be obtained by the following equation:

$$\begin{cases} \|V(t_s^+)\| = \sqrt{(V_d(t_s^+))^2 + (V_q(t_s^+))^2 + (V_o(t_s^+))^2} \\ \|V(t_s^-)\| = \sqrt{(V_d(t_s^-))^2 + (V_q(t_s^-))^2 + (V_o(t_s^-))^2} \end{cases} \quad (20)$$

According to (20), the magnitude of the space vector after switching instant of the capacitor bank in frame (dqo) can be written similar to (21) and the equivalent inductance of branches (2) and (3) can be calculated based on the magnitude of the space vector of the voltage before and after switching instant.

$$\|V(t_s^+)\| = \frac{L_{eq}}{L_{th} + L_{eq}} \|V(t_s^-)\| \Rightarrow L_{eq} = \frac{\|V(t_s^+)\|}{\|V(t_s^-)\| - \|V(t_s^+)\|} \times L_{th} \quad (21)$$

$$K_{s,v} = \frac{\|V(t_s^+)\|}{\|V(t_s^-)\| - \|V(t_s^+)\|} \Rightarrow L_{eq} = K_{s,v} \times L_{th} \quad (22)$$

Regarding (9) and (22),  $L_2$  can be derived as follows:

$$\Rightarrow L_2 = k_{s,v} \frac{L_{th} L_3}{L_3 + L_{th}} \quad (23)$$

Considering (14), as branches (2) and (3) are parallel, it can be written at the instant ( $t_s^+$ ):

$$V_{abc}(t_s^+) = L_2 \frac{di_{2abc}(t_s^+)}{dt} = L_3 \frac{di_{3abc}(t_s^+)}{dt} \quad (24)$$

Similar to (19), by transferring from triaxial apparatus (abc) to the frame (dqo), the voltage in the frame (dqo) is introduced as follows:

$$\begin{cases} (V_{abc}(t_s^+) = L_2 \frac{di_{2abc}(t_s^+)}{dt}) \times K \\ (V_{abc}(t_s^+) = L_3 \frac{di_{3abc}(t_s^+)}{dt}) \times K \end{cases}$$

$$\Rightarrow \begin{cases} V_{qdo}(t_s^+) = KL_2 K^{-1} i_{dqo_2}(t_s^+) + KL_2 K^{-1} \frac{di_{dqo_2}(t_s^+)}{dt} \\ V_{qdo}(t_s^+) = KL_3 K^{-1} i_{dqo_3}(t_s^+) + KL_3 K^{-1} \frac{di_{dqo_3}(t_s^+)}{dt} \end{cases} \quad (25)$$

On the other hand, it can be simplified as follows:

$$KLK^{-1} = \omega \begin{bmatrix} 0 & 1 & 0 \\ -1 & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} i_q \\ i_d \\ i_0 \end{bmatrix}, \quad KLK^{-1} = L \quad (26)$$

In (25),  $K.L.K^{-1}.i_{dq}$  is called voltage motion parameter which is circulating with speed ( $\omega_e$ ) under the system nominal frequency ( $f_{sys}=60(\text{Hz})$ ) and  $(K.L.K^{-1}di_{dqo}/dt)$  is the transient state voltage of the system which is considered with speed ( $\omega_d$ ). Switching frequency of the capacitor bank is in the range (300-1000) hertz [7], therefore signal frequency in the transient state is also in the same range. In this state, approximately, the ratio ( $\omega_e/\omega_d$ ) is almost between 5 to 15. In this condition, the voltage motion parameter  $K.L.K^{-1}.i_{dq}$  versus the transient state voltage of the system  $K.L.K^{-1}di_{dqo}/dt$  can be ignored. Therefore, according to the above mentioned discussions, voltage equation in the monitoring location in the reference frame (dqo) is introduced as (27).

$$V_{qdo}(t_s^+) = L_2 \frac{di_{dqo_2}(t_s^+)}{dt} = L_3 \frac{di_{dqo_3}(t_s^+)}{dt} \quad (27)$$

$$\|V\| = L_2 \left\| \frac{di_2(t_s^+)}{dt} \right\| = L_3 \left\| \frac{di_3(t_s^+)}{dt} \right\| \quad (28)$$

$$\frac{L_2}{L_3} = \left\| \frac{di_3(t_s^+)}{di_2(t_s^+)} \right\| = \left\| \frac{i_3(t_s^{++}) - i_3(t_s^+)}{di_2(t_s^{++}) - di_2(t_s^+)} \right\| = \Psi_{s,v} \quad (29)$$

In (29),  $\|i_3(t_s^{++})\|$  is the magnitude of the current space vector of branch (3) immediately after switching instant ( $t_s^+$ ) and  $\|i_2(t_s^{++})\|$  is the magnitude of the current space vector of branch (2) immediately after switching at the instant ( $t_s^+$ ). The parameter ( $\Psi_{s,v}$ ) is defined as ratio of changes in the magnitude of the current space vector of branch (3) to changes in the magnitude of the current space vector of branch (2) after capacitor bank switching.

Regarding (23) and (29), the inductance of branch (2) is written based on changes in the magnitude of the voltage space vector before and after switching, the ratio of changes in the magnitude of the current space vector of branches (2) and (3) and thevenin inductance.

$$L_2 = K_{s,v} . L_{th} (1 + \Psi_{s,v}) \quad (30)$$

On the other hand, the inductance of branch (2) can be written as follows:

$$L_2 = d_2 \times L_u \Rightarrow d_2 = K_{s,v} \times \frac{L_{th}}{L_u} (1 + \Psi_{s,v}) \quad (31)$$

In this case, the distance of switched capacitor bank from the monitoring location in the domain of space vector is estimated by (31). Similar to (17), to generalize to derived an equation in the space

domain, according to the Fig. 13, if there are the (n) branches in the monitoring location, the distance between the switched capacitor bank and the monitoring location ( $d_2$ ) is calculated based on relation (32).

$$d_2 = K_{sv} \times \frac{L_{th}}{L_u} (1 + \Psi_{sv_1} + \Psi_{sv_2} + \dots + \Psi_{sv_n}) \tag{32}$$

### 5. Simulation Result

In this section, effectiveness and applicability of the method in finding direction and estimating the distance of the switched capacitor bank in different situations (normal and Back-To-Back), in the presence of stochastic nonlinear loads and change of sampling rate in IEEE-13 Bus system are studied by PSCAD. The specifications of this network (Fig. 14) were reported in [12].

#### 5.1. Capacitor bank switching in the normal state (except back-to-back condition)

In this system, a 300-kVAr three-phase capacitor bank located on bus 633 is switched on and the distance between the switching capacitor bank (bus 633) and the monitoring location (bus 632) is considered 0.1 mile in simulations (Fig. 15). In this case, suppose that the fixed capacitor does not exist in any other buses. In order to monitor all branches, P.Q.M.s are located on buses 632 and 671. Table 3 show the indicator of the space vector of the current and voltage ( $\|D_{V1}\|$ ) for all branches to find direction the switched capacitor bank. Table 4 indicates the distance between the switched capacitor bank and the monitoring location in the desired branch.

Regarding the flowchart of finding direction of capacitor bank based on gradient of the transient voltage and current space vector (3-c), since the switched capacitor bank in the system under study is in branch (2), therefore the polarity of the indicator of the space vector ( $\|D_{V1}\|$ ) for the branches (1) and (2) is negative and it is positive in other branches. Moreover, the value of indicator  $\|D_{V1}\|$  in branch (2) is larger than the indicator  $\|D_{V1}\|$  in other branches (here, branch 1). The results of Table 3 shows the above mentioned issues.

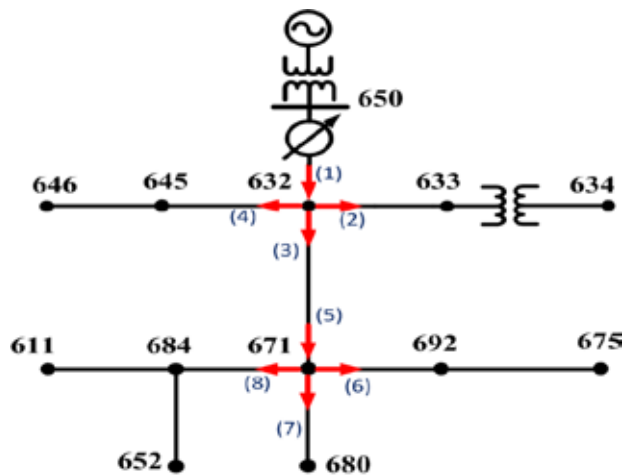


Fig. 14. IEEE-13 Bus Distribution network



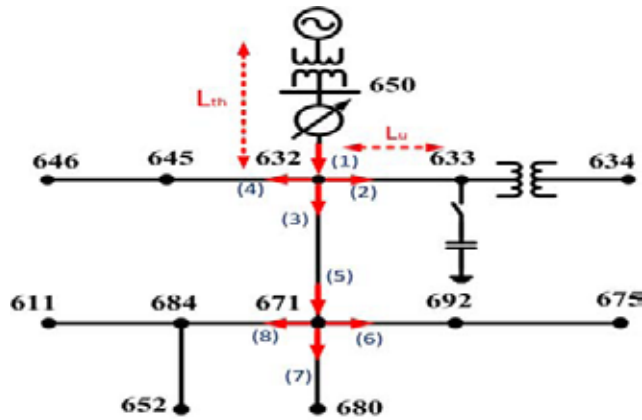


Fig. 15. Capacitor bank switching at bus 633 in normal switching state

Table 3. Determining direction of the switched capacitor bank at bus 633 by  $IID_{VI}II$  in normal switching state

Branch	$  V(t_s^-)  ,   V(t_s^+)  $	$  I(t_s^-)  ,   I(t_s^+)  $	$  D_{VI}  $
1	(3.4663,1.1947)	(0.7610,0.9818)	-0.4993
2	(3.4663,1.1947)	(0.0836,0.5875)	-1.1260
3	(3.4663,1.1947)	(0.6582,0.5150)	+0.3199
4	(3.4663,1.1947)	(0.2053,0.0936)	+0.2496
5	(3.4986,1.1804)	(0.4678,0.3719)	+0.1468
6	(3.4986,1.1804)	(0.1837,0.1742)	+0.0145
7	(3.4986,1.1804)	(0,0)	---
8	(3.4986,1.1804)	(0.0299,0.0154)	+0.022
9	(3.4986,1.1804)	(0,0)	---

Regarding the technique proposed in section 4, the exact location of the switched capacitor bank is determined by the magnitude of the space vector of the transient voltage and current.  $L_u$  is the inductance per-unit length of line (2), which assumed to be known. The inductance of the main feeder from the monitoring location at bus 632 to the source (branch (1)), is considered as  $L_{th}$ . Parameters  $L_u$  and  $L_{th}$  are calculated based on the specification of network IEEE-13 bus [12]. It can be seen in Table 4, the error of distance estimation is 7.3 % approximately.

As shown in Fig. 16, if the switching capacitor bank is removed from bus 633 and placed in 0.15 mile from monitoring location on the bus 671 (the distance between buses 692, 675 is considered 0.15 mile). In this case, Tables 5 and 6 show simulation results for the calculation of  $IID_{VI}II$  and estimated distance of the switched capacitor bank from the monitoring location in normal switching state respectively.

In Fig. 16, since the switched capacitor bank in the system under study is in branch (6), therefore the polarity of the indicator of the space vector ( $IID_{VI}II$ ) for the branches (1), (3), (5) and (6) should be negative and positive in other branches. Moreover, the value of indicator  $IID_{VI}II$  in branch (6) is larger

Table 4. Estimating the distance between the switched capacitor bank at bus 633 and the monitoring location at bus 632 in normal switching state

Error	$D_{real}$	$D_{est}$	$\Psi_2$	$\Psi_1$	$K_{SV}$	$\ V(t_s^-, t_s^+)\ $
7.3 %	0.1	0.1071	-0.028	-0.201	0.52	(3.466, 1.1947)

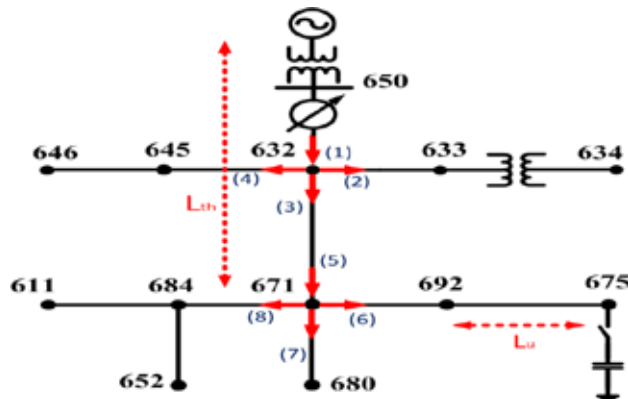


Fig. 16. Capacitor bank switching at bus 675 in normal switching state

Table 5. Determining direction of the switched capacitor bank at bus 675 by  $\|D_{VI}\|$  in normal switching state

Branch	$\ V(t_s^-)\ , \ V(t_s^+)\ $	$\ I(t_s^-)\ , \ I(t_s^+)\ $	$\ D_{VI}\ $
1	(3.4663, 2.3486)	(0.7610, 0.7972)	-0.0404
2	(3.4663, 2.3486)	(0.0836, 0.0751)	+9.5E-3
3	(3.4663, 2.3486)	(0.6583, 0.7478)	-0.1
4	(3.4663, 2.3486)	(0.2053, 0.1621)	+0.0482
5	(3.3917, 0.61)	(0.4678, 0.561)	-0.2592
6	(3.3917, 0.61)	(0.1837, 0.4895)	-0.8506
7	(3.3917, 0.61)	(0, 0)	---
8	(3.3917, 0.61)	(0.0308, 0.02588)	+0.0136
9	(3.3917, 0.61)	(0, 0)	---

Table 6. Estimating the distance between the switched capacitor bank at bus 675 and the monitoring location at bus 671 in normal switching state

Error	$D_{real}$	$D_{est}$	$\Psi_2$	$\Psi_1$	$K_{SV}$	$\ V(t_s^-, t_s^+)\ $
8.1 %	0.15	0.1621	0	-0.35	0.22	(3.3917, 0.61)

than the indicator  $IID_{VI}$  in other branches (here, branches 1, 3, 5). The results in Table 5 satisfies the above mentioned expectations.

Table 5 shows the estimate distance of switched capacitor bank based on magnitude of space vector voltage and current transients at bus 675.

From the Tables 5 and 6, it can be seen that under the normal capacitor bank switching at various locations, determining direction and estimating the distance between the switched capacitor bank and the monitoring location have been, properly done.

### 5.2. Capacitor bank switching in the back-to-back switching state

Switching of capacitor bank, in the presence of the fixed capacitor bank, known as back-to-back switching in the studied network. The initial inrush currents of the capacitors which were successively connected in a back-to-back scenario were limited by using series inductors with impedances typically less than 1 % of the capacitors reactance. In the back-to-back state, because of resonance between the limiter inductor and capacitors, the frequency of the transient state increases compared to the normal switching state; In the normal switching, such transient states in high frequency are not created, due to the higher inductance of the network in the instant of switching.

To study algorithm in the back-to-back switching conditions, In the first case, as shown in Fig. 17, the capacitor bank placed in bus 633 in presence of a fixed capacitor bank, 200-kVAR three-phases located in bus 692 is switched on and In the next case which is represented in Fig. 18, the capacitor bank placed in bus 675 in presence of a fixed capacitor bank, 200-kVAR three-phases mounted in bus 633 is switched on. Tables 7, 9 shows the index from the space vector of the transient current and voltage ( $IID_{VI}$ ) to find direction the switching the capacitor bank and Table 8, 10 indicates the results of estimating the distance between the switched capacitor bank and the monitoring location in the mentioned branches.

The results represented in Tables 7, 9 show that back-to-back switching can affect only the values of indices compared to normal switching and not the method accuracy. It can be seen in Tables 8, 10 that the error of estimating the distance in the back-to-back switching increased compared to the normal switching. The proposed method in different conditions (normal and back-to-back) were studied

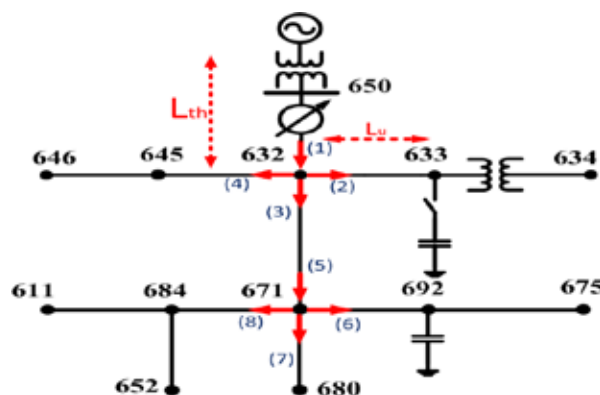


Fig. 17. Capacitor bank switching at bus 633 in the Back-To-Back switching state

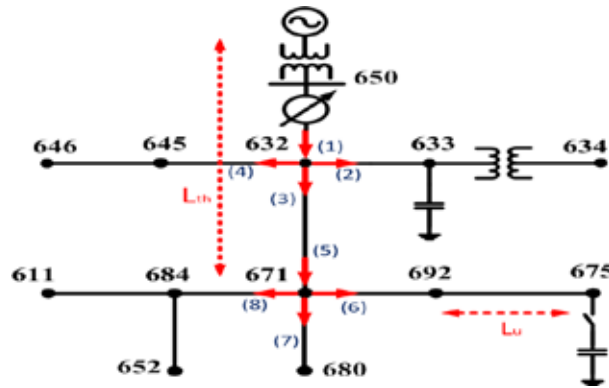


Fig. 18. Capacitor bank switching at bus 675 in the Back-To-Back switching state

Table 7. Determining direction of the switched capacitor bank at bus 633 using  $\|D_{VI}\|$  in Back-To-Back switching state

Branch	$\ V(t_s^-)\ , \ V(t_s^+)\ $	$\ I(t_s^-)\ , \ I(t_s^+)\ $	$\ D_{VI}\ $
1	(3.4964,1.4459)	(0.6882,0.8240)	-0.2784
2	(3.4964,1.4459)	(0.0829,0.4650)	-0.7834
3	(3.4964,1.4459)	(0.4461,0.3147)	+0.2694
4	(3.4964,1.4459)	(0.1645,0.0885)	+0.1558
5	(3.4525,3.4119)	(0.3878,0.2679)	+4.86E-3
6	(3.4525,3.4119)	(0.1040,0.0646)	+1.59E-3
7	(3.4525,3.4119)	(0,0)	---
8	(3.4525,3.4119)	(0.0289,0.0268)	+8.52E-5
9	(3.4525,3.4119)	(0,0)	---

Table 8. Estimating the distance between the switched capacitor bank at bus 633 and the monitoring location at bus 632 in Back-To-Back switching

Error	$D_{real}$	$D_{est}$	$\Psi_2$	$\Psi_1$	$K_{SV}$	$\ V(t_s^-, t_s^+)\ $
12.1 %	0.1	0.1121	-0.047	-0.36	0.705	(3.496,1.4459)

under different situations such as changes in the size and location of the switched capacitor bank and simulation results verified the effectiveness of the proposed algorithm.

### 5.3. Capacitor bank switching in presence of stochastic nonlinear load

Regarding the fact that the transient states of the systems with capacitors could have unpredictable behavior in the presence of stochastic nonlinear loads and affect the analysis in the proposed algorithm, in this section, in addition to PQ loads with nonlinear nature, the proposed algorithm will be tested by adding a three-phase electric arc furnace with power of 0.65 MVA, in bus

Table 9. Determining direction of the switched capacitor bank at bus 675 using IID<sub>V1</sub>II in Back-To-Back switching

Branch	IIV(t <sub>s</sub> )II , IIV(t <sub>s</sub> <sup>+</sup> )II	III(t <sub>s</sub> )II , III(t <sub>s</sub> <sup>+</sup> )II	D <sub>V1</sub>
1	(3.4078,2.9103)	(0.6889,0.7053)	-8.159E-3
2	(3.4078,2.9103)	(0.0958,0.0678)	+0.0139
3	(3.4078,2.9103)	(0.5223,0.5996)	-0.0384
4	(3.4078,2.9103)	(0.1646,0.1489)	+7.81E-4
5	(3.4217,0.6508)	(0.4640,0.5442)	-0.2222
6	(3.4217,0.6508)	(0.1827,0.4781)	-0.8185
7	(3.4217,0.6508)	(0,0)	---
8	(3.4217,0.6508)	(0.0302,0.02513)	+0.01402
9	(3.4217,0.6508)	(0,0)	---

Table 10. Estimating the distance between the switched capacitor bank at bus 675 and the monitoring location at bus 671 in Back-To-Back switching state

Error	D <sub>real</sub>	D <sub>est</sub>	Ψ <sub>2</sub>	Ψ <sub>1</sub>	K <sub>S,V</sub>	(V(t <sub>s</sub> <sup>-</sup> ,t <sub>s</sub> <sup>+</sup> )
15.6 %	0.15	0.1725	0	-0.28	0.234	(3.4217,0.650)

Table 11. Determining direction of the capacitor bank using IID<sub>V1</sub>II in the presence of stochastic nonlinear loads in the normal switching state

Branch	IIV(t <sub>s</sub> )II , IIV(t <sub>s</sub> <sup>+</sup> )II	III(t <sub>s</sub> )II , III(t <sub>s</sub> <sup>+</sup> )II	D <sub>V1</sub>
1	(3.4180,1.2359)	(1.0264,1.2291)	-0.4423
2	(3.4180,1.2359)	(0.0848,0.5768)	-1.0735
3	(3.4180,1.2359)	(0.9909,0.8357)	+0.3386
4	(3.4180,1.2359)	(0.2043,0.0938)	+0.2411
5	(3.2988,1.9167)	(0.7325,0.6269)	+0.1459
6	(3.2988,1.9167)	(0.1899,0.1809)	+0.0124
7	(3.2988,1.9167)	(0.2453,0.2397)	+7.73E-3
8	(3.2988,1.9167)	(0.0356,0.0193)	+0.0225
9	(3.2773,1.9307)	(0.2453,0.2397)	+0.073

680. Tables 11, 12, 13 and 14 show respectively the capacitor bank direction and estimation of distance between the switched capacitor bank and the monitoring location in different switching states.

#### 5.4. Investigating of sampling rate in estimating distance of the switched capacitor bank

The precision of distance estimation of a switched capacitor bank is a function of the sampling rate measured by PQM in the monitoring locations. The high sampling rate causes the time between

Table 12. Estimation of distance between the switched capacitor bank in the presence of stochastic nonlinear loads in the normal switching state

Error	$D_{real}$	$D_{est}$	$\Psi_2$	$\Psi_1$	$K_{sV}$	$\ (V(t_s^-, t_s^+)\ $
9.2 %	0.1	0.1092	-0.02	-0.26	0.566	(3.418,1.235)

Table 13. Determining direction of the switched capacitor bank using IID<sub>V<sub>I</sub></sub>II in the presence of stochastic nonlinear loads in the Back-To-Back switching state

Branch	IIV(t <sub>s</sub> <sup>-</sup> )II , IIV(t <sub>s</sub> <sup>+</sup> )II	III(t <sub>s</sub> <sup>-</sup> )II , III(t <sub>s</sub> <sup>+</sup> )II	$\ D_{VI}\ $
1	(3.4310,1.4451)	(1.0264,1.2291)	-0.2549
2	(3.4310,1.4451)	(0.0848,0.5768)	-0.7586
3	(3.4310,1.4451)	(0.9909,0.8357)	+0.3129
4	(3.4310,1.4451)	(0.2043,0.0938)	+0.1888
5	(3.3201,3.2684)	(0.7325,0.6269)	+5.89E-4
6	(3.3201,3.2684)	(0.1899,0.1809)	+4.23E-4
7	(3.3201,3.2684)	(0.2453,0.2397)	+7.23E-5
8	(3.3201,3.2684)	(0.0356,0.0193)	+7.55E-5
9	(3.2967,3.2468)	(0.2453,0.2397)	+7.11E-5

Table 14. Estimation of distance between the switched capacitor bank in the presence of stochastic nonlinear loads in the Back-To-Back switching state

Error	$D_{real}$	$D_{est}$	$\Psi_2$	$\Psi_1$	$K_{sV}$	$\ (V(t_s^-, t_s^+)\ $
5.8 %	0.1	0.1058	-0.044	-0.417	0.732	(3.431,1.4451)

two successive samples to become smaller and the current and voltage changes measured immediately after switching instant to be more precise and as a result, the distance is estimated better. Tables 15 and 16 show respectively the effect of the sampling rate on estimating distance of the switched capacitor bank in normal switching and back-to-back switching states.

Manifolding the sampling rate will make the distance estimation more precise, but, it is noteworthy that in practice, careful measures should be also taken to remove the high frequency noisy signals mainly arising from the shunt capacitance of the feeder lines. The higher the sampling rate, the more complicated this filtering. This will be a drawback of increasing the sampling rate. To prevent occurrence of such cases, the sampling frequency should be limited [6]. Such a limit is basically determined by the highest frequency of the main signal during the switching transients [11]. The transient state frequency is usually in the range of 300-1000 Hz, but increase of frequency of the transient state produced by the back-to-back switching should also be considered. As it's depicted in [18], frequency of 60 KHz as the maximum sampling rate for algorithm of distance estimation of the capacitor bank is considered in this paper.

Table 15. Effect of measured signals sampling rate on estimating distance in the normal switching state

	256 Sample/cycle	512 Sample/cycle	1024 Sample/cycle
$(\ IV(t_s^-)\ , \ IV(t_s^+)\ )$	(3.4655,1.2223)	(3.4659,1.2086)	(3.4663,1.1947)
$K_{S,V}$	0.544	0.534	0.52
$(1+ \Psi_{1+} \Psi_2)$	0.83	0.803	0.771
$D_{est}$	0.1210	0.1149	0.1073
Error %	21 %	14.9 %	7.3 %

Table 16. Effect of measured signals sampling rate on estimating distance in the Back-To-Back switching state

	256 Sample/cycle	512 Sample/cycle	1024 Sample/cycle
$(\ IV(t_s^-)\ , \ IV(t_s^+)\ )$	(3.4955,1.4689)	(3.4960,1.4567)	(3.4964,1.4459)
$K_{S,V}$	0.724	0.714	0.705
$(1+ \Psi_{1+} \Psi_2)$	0.65	0.62	0.593
$D_{est}$	0.1262	0.1187	0.1121
Error %	26.2 %	18.7 %	12.1 %

### 6. Comparison of the Results between the Proposed Algorithm whit Pervious Methods

In this section, toward determining the location of a 3-phase switched capacitor bank (300 (kvar)) at bus 633, the results taken from algorithm based on the space vector is compared with the algorithm based on each phase calculating in the 13-bus network and under the same conditions, in the simplest switching state (normal switching) [10].

Regarding the fact that capacitor bank switching can happen at every instant and in every situation, and since the proposed algorithm should cover all existing states and times, thus two different switching instants (switching in the voltage peak and switching at the instant of voltage zero crossing) are compared.

Table 17 shows the results obtained from determining direction of switched capacitor bank at bus 633 at the instant when voltage in phase (b) pass through the peak ( $t_s=0.5222$ ) in the method based on each phase and the method based on space vector analysis.

According to Fig. 15, since the switched capacitor bank is located at bus 633, thus the calculated index in branches (1) and (2) should be negative and in other branches should be positive. Moreover, the index value in branch (2) should be larger than the one in branch (1).

According to Table 17, at the instant when voltage passing the peak, both indices  $D_{V1}$  and  $\|D_{V1}\|$  identified the switched capacitor bank direction correctly.

The results obtained from finding direction of switched capacitor bank at the instant when voltage of phase (b) at zero crossing instant by two indices  $D_{V1}$  and  $\|D_{V1}\|$  are shown in **Table 18**.

It can be seen from the highlighted values in **Table 18** that index identified the switched capacitor bank direction correctly but  $D_{V1}$  index identified mistakenly the switched capacitor bank direction in

Table 17. Comparison of results in determining direction of switched capacitor bank using indices  $D_{V1}$  and  $IID_{V1II}$  (switching in phase (B) peak instant)

Branch	state(a)		state(b)	
	$D_{V1}$	Sign	$IID_{V1II}$	Sign
1	-0.6240	Negative	-0.4933	Negative
2	-1.3518	Negative	-1.1260	Negative
3	+0.4520	Positive	+0.3199	Positive
4	+0.3155	Positive	+0.2496	Positive
5	+0.2887	Positive	+0.1456	Positive
6	+0.0521	Positive	+0.0145	Positive
7	---	---	---	---
8	+0.0236	Positive	+0.0222	Positive
9	---	---	---	---

Table 18. Comparison of the results obtained from finding direction of switched capacitor bank by two indices  $D_{V1}$  and  $IID_{V1II}$  (switching at the instant when voltage of phase (B) at zero crossing instant)

Branch	state(a)		state(b)	
	$D_{V1}$	Sign	$IID_{V1II}$	Sign
1	-1.79 E-5	Negative	-0.3437	Negative
2	-3.13 E-4	Negative	-0.6749	Negative
3	+2.69 E-5	Positive	+0.3564	Positive
4	+3.58 E-4	Positive	+0.0528	Positive
5	-3.36 E-4	Negative	+0.0806	Positive
6	+3.64 E-6	Positive	+0.0355	Positive
7	---	---	---	---
8	+4.74 E-5	Positive	+0.0179	Positive
9	---	---	---	---

branch (5) instead of branch (2). Comparing Tables 17 and 18, it can be concluded that the index based on the slope of the voltage and current space vectors ( $IID_{V1II}$ ), in spite of to the index based on the slope of the voltage and current in each phase, identified correctly the switched capacitor bank direction, because all three-phase quantities contribute to the magnitude of the space vector and this method is almost independent from the switching instant.

Table 19 shows the results obtained from estimation of distance between the switched capacitor bank at bus 633 and the monitoring location at bus 632 at the instant when the voltage of phase (b) passing the peak value.

In simulations, the distance between the switching capacitor bank at bus 633 and the monitoring location at bus 632 is considered as 0.1 mile.

As it can be seen in Table 19, unlike to the previous proposed methods, considering the reactance laterals branches, the distance between the switched capacitor bank and the monitoring location at the



Table 19. Comparison of the results obtained from estimation of distance between the switched capacitor bank at the instant when the voltage passing the peak

Error	$D_{est}$	$(\Psi_1, \Psi_2)$	K	
34 %	0.1340	( 0 , 0 )	0.5	distance Estimated in the method based on each phase
7.3 %	0.1073	(-0.201,-0.028)	0.52	distance Estimated in the method based on space vector(proposed method)

Table 20. Comparison of the results obtained from estimation of distance of the switched capacitor bank at the instant when the voltage of phase (B) passing through the zero

Error	$D_{est}$	$(\Psi_1, \Psi_2)$	K	Condition
58.7 %	0.1587	( 0 , 0 )	0.592	distance Estimated in the method based on each phase
9.5 %	0.1095	(-0.32 , -0.01)	0.61	distance Estimated in the method based on space vector(proposed method)

instant when the voltage phase (b) passing through the peak is estimated with an acceptable error in the method based on each phase and the method based on the space vector.

Table 20 shows the results obtained from estimation of distance between the switched capacitor bank at bus 633 and the monitoring location at bus 632 at the zero crossing instant of phase b.

Comparing the results obtained from Table 20, it can be seen that the error of distance estimation at the zero crossing instant in the method based on each phase is higher than the error of distance estimation in the method based on the space vector dramatically.

## 7. Conclusion

This paper proposed an efficient and novel technique for assessment of the direction of switched capacitor bank and estimating its distance from the monitoring location in real distribution systems. In this method, at first the direction in which the capacitor is switched determined, based on the slopes of the voltage and current space vectors immediately after and before switching. Then, the location of the switched capacitor bank is determined using initial changes in magnitude of the space vector of the current and voltage after instant of switching as well as estimation of the inductance of the desired line. The validity and effectiveness of the proposed algorithm, in different switching states (normal and back-to-back), considering stochastic nonlinear loads and different sampling rate were studied and Simulation results show lower and acceptable error in locating the switched capacitor bank in distribution systems compared than other previous conventional method. The advantages of the proposed method are as follows:

- Due to calculating of proposed indices based on switching transient, other parameters such as harmonics, load changes, faults, etc. can't have significant error in method accuracy. However, in the previous works, the introduced indices have affected dramatically by the above mentioned factors.

- The proposed method is applied based on switching transient voltage and current in the switching instant exactly. Therefore, this method can determine the switched capacitor bank and estimate its location from the monitoring location immediately after the switching.
- Application of space vector magnitude of current and voltage improved the distance estimation algorithm function due to using the information of three phases in the calculations and the estimation of distance will be independent from the switching instant.
- The required calculations for determining the switched capacitor banks are simple and can be applied in real distribution systems.

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